

US Department of Commerce
Patent and Trademark Office

Form PTO-1449 (Modified)

Atty. Docket No.:
042390.P1674C2D

Application No.:
09/707,448

Applicant:
Jack D. Pippin

Filing Date:
November 7, 2000

US Patent Documents

Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date

Foreign Patent Documents

Examiner's Initials	Date	Document Number	Country	Class	Sub- Class	Translation
CRUR	4/20/03	0523736A1	European	G01R	31/28	

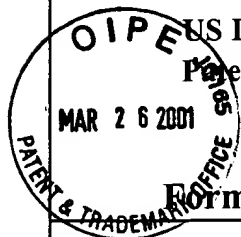
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

Examiner *Carlos P. Pippin*

Date Considered *6/14/04*

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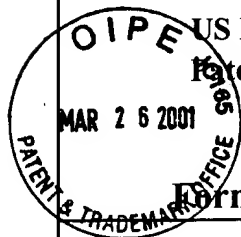
Applicant:
Pippin, J.

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US Patent Documents

Examiner's Initials	Date	Document Number	Name	Class	Sub-Class	Filing Date
CROR		4,432,031	Premierlani	361	97	
CROR	5/11/04	4,442,972	Sahay et al.	236	1EA	
CROR		4,488,824	Salem	374	181	
CROR		5,077,491	Heck et al.	307	354	
CROR		5,291,607	Ristic et al.	395	750	
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CROR		5,255,149	Matsuo	361	103	
CROR		5,560,017	Barrett et al.	395	733	
CROR		5,440,305	Signore et al.	341	120	
CROR		5,253,938	Stixrud	374	173	
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CROR		5,008,771	Palera	361	103	
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CROR		4,851,987	Day	395	560	
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		4,903,106	Fukunaga et al.	357	43	
Examiner	Date Considered 6/14/04					

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CROR	4/14	4,935,864	Schmidt et al.	363	141	
CROR		5,087,870	Salesky et al.	323	276	
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CROR		5,283,631	Koerner et al.	307	451	
CROR		5,325,256	Weng et al.	364	557	
CROR		5,359,236	Giordano et al.	307	310	
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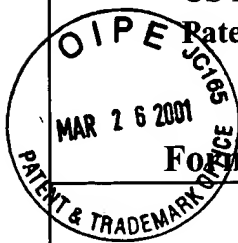
CROR	Chin, Shu-Yuan and Chung-Yu Wu, "A New Type of Curvature-Compensated CMOS Bandgap Voltage References, VLSITSA, 1991, pps. 398-402.
CROR	Ferro, Marco, Franco Salerno and Rinaldo Castello, "A Floating CMOS Bandgap Voltage Reference for Differential Applications", IEEE: Journal of Solid-State Circuits, Vol. 24, No. 3, June 1989, pps. 690-697.
CROR	Salminen, O. and K. Halonen, "The Higher Order Temperature Compensation of Bandgap Voltage References", IEEE, 1992, pps. 1388-1391.
CROR	Allen and Holberg, "CMOS Analog Circuit Design", H.P.W., pp. 539-549, 1987.
CROR	Analog Devices, Low Power, Programmable Temperature Controller, pps. 1-16, 1995.
CROR	Sanchez et al., "Thermal Management System for High Performance PowerPC Microprocessors", pps. 325-330, 1997.

Examiner *Charles R. Kelly, Jr.*

Date Considered *6/14/04*

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Atty. Docket No.:
423901674C2D

Application No.:
09/707,448

Applicant:
Pippin, J.

Filing Date:
November 7, 2000

US Patent Documents

Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
CROR	4/1/01	5,085,526	Sawtell et al.	374	101	
CROR		5,195,827	Audy et al.	374	172	
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CROR		5,490,059	Mahalingaiah, et al.	364	166	
CROR		5,723,998	Saito, et al.	327	513	
CROR		5,485,127	Bertoluzzi et al.	331	69	
CROR		4,893,097	Zwack	331	176	
CROR		5,798,667	Herbert	327	513	

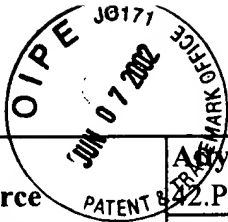
Foreign Patent Documents

Examiner's Initials	Date	Document Number	Country	Class	Sub- Class	Translation
CROR	4/1/01	0683558A1	EPO			

Examiner Charles R. Alfrey Date Considered 8/14/04

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US Department of Commerce Patent and Trademark Office		App. Docket No.: 42.P1674C2D				Application No.: 09/707,448	
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		Filing Date: November 7, 2000					
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Examiner's Initials		Date	Document Number	Name	Class	Sub- Class	Filing Date
CLUR		09/08/98	5,805,403	Chemla	361	103	03/28/96
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Examiner's Initials		Date	Document Number	Country	Class	Sub- Class	Translation
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
Examiner <i>Charles A. Rodriguez</i>				Date Considered <i>6/14/04</i>			

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(37 C.F.R. § 1.8 (a))

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On May 31, 2002
Date of Deposit

April Morrey
Name of Person Mailing Correspondence

Charles A. Rodriguez 5/31/02
Signature Date

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Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 238663US25DIV		SERIAL NO. 09/707,448	
<div style="display: flex; align-items: center; justify-content: center;"> <div> LIST OF REFERENCES CITED BY APPLICANT </div> </div>				APPLICANT Jack D PIPPIN			
				FILING DATE November 7, 2000			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
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	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>CROR</i>	AW	Intel Corporation, "Advanced Power Management (APM)- BIOS Interface Specification," Revision 1.2, February 1996					
	AX						
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner <i>[Signature]</i>					Date Considered <i>6/14/04</i>		
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